

Notice of Allowability

Applicati n N .

10/612,194

Examiner

TAN N. TRAN

Applicant(s)

MOULI, CHANDRA

Art Unit

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to amendment filed on 03/24/06.
2. ☒ The allowed claim(s) is/are 1-12, 14, 20-36 and 60-69.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____

DETAILED ACTION

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Elizabeth Parsons on 5/19/06.

The application has been amended as follows:

Cancel the non-elected claims 15-19.

In claim 1, lines 4,5, "at least one transistor structure, the at least one transistor structure" should be changed to -- at least single transistor structure, the at least single transistor structure --

In claim 1, line 10, "the at least one transistor structure" should be changed to -- the at least single transistor structure --

In claim 1, line 12, "the transistor structure" should be changed to -- the single transistor structure --

In claims 2,3,4,7, lines 1,2, "the at least one transistor structure" should be changed to -- the at least single transistor structure --

In claim 9, lines 1,2, "the transistor structure" should be changed to -- the single transistor structure --

In claim 10, line 1, “the transistor structure” should be changed to – the single transistor structure --

In claim 11, line 2, “the at least one transistor structure” should be changed to – the at least single transistor structure --

In claim 20, line 4, “at least one transistor structure” should be changed to – at least single transistor structure --

In claim 20, lines 4,5, “the at least one transistor structure” should be changed to – the at least single transistor structure --

In claim 20, line 14, “the transistor structure” should be changed to – the single transistor structure --

In claims 21,22, lines 1,2, “the at least one transistor structure” should be changed to – the at least single transistor structure --

In claim 25, line 5, “a transistor structure, the transistor structure” should be changed to – a single transistor structure, the single transistor structure --

In claim 25, lines 11,13, “the transistor structure” should be changed to – the single transistor structure --

In claims 26-28,31,33,34, line 1, “the transistor structure” should be changed to – the single transistor structure --

In claim 39, lines 8,13,15, “the transistor structure” should be changed to – the single transistor structure --

In claims 60,61, lines 1,2, “the transistor structure” should be changed to – the single transistor structure --

In claims 64-69, lines 1,2, "at least one of the at least one transistor structure" should be changed to -- the at least single transistor structure --

Reasons for Allowance

2. The following is an examiner's statement of reasons for allowance:

Claims 1-12,14,20-36,60-69 are allowable over the reference of record because none of these references disclose or can be combined to yield the claimed invention such as the at least single transistor structure is configured to have at least two threshold voltages associated with the at least one channel region at a same time and wherein a current-voltage characteristic of the single transistor structure is determined at least in part by the threshold voltages as recited in claims 1,25,39, the single transistor structure having the normal conduction path is associated with a highest first threshold voltage as recited in claim 20.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

3. Any inquiry concerning this communication or earlier communication from the examiner should be directed to Tan Tran whose telephone number is (571) 272-1923. The examiner can normally be reached on M-F 8:30AM-5PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nathan Flynn can be reached on (571) 272-1915. The fax phone numbers for the

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organization where this application or proceeding is assigned are (571) 273-8300 for regular communications and (571) 273-8300 for after final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-3900.

TT

May 2006